

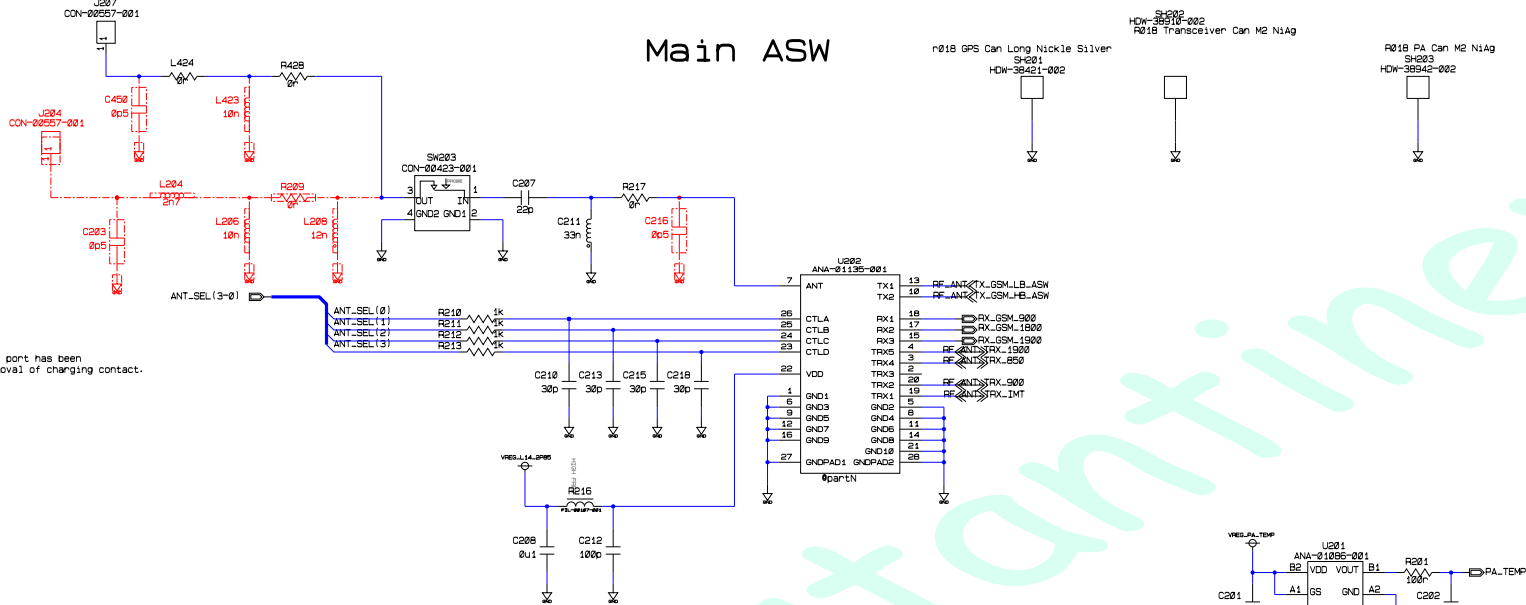
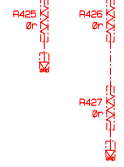
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HIERARCHICAL BLOCK	COPYRIGHT 2010 RESEARCH IN MOTION LIMITED 295 PHILIP ST. WATERLOO, ONTARIO CANADA
DRAWN BY Thanh Hua	PHONE (519) 888-7465, FAX (519) 888-6986
RWD CHECK Thanh Hua	TITLE R005 B125/6 w 768-MB DDR
MFG CHECK Thanh Hua	Uses PCB-31275-005
EDITED BY: y111	SIZE DRAWING No. SCH-39388-002
LAST EDIT: 2010/12/15 22:37:08	REV B SHEET 1 of 20

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### Main ASW

Left side Flap which is close to Main Antenna connected to Main GND thru ckt. below (top GND to be replaced with Flap geometry)

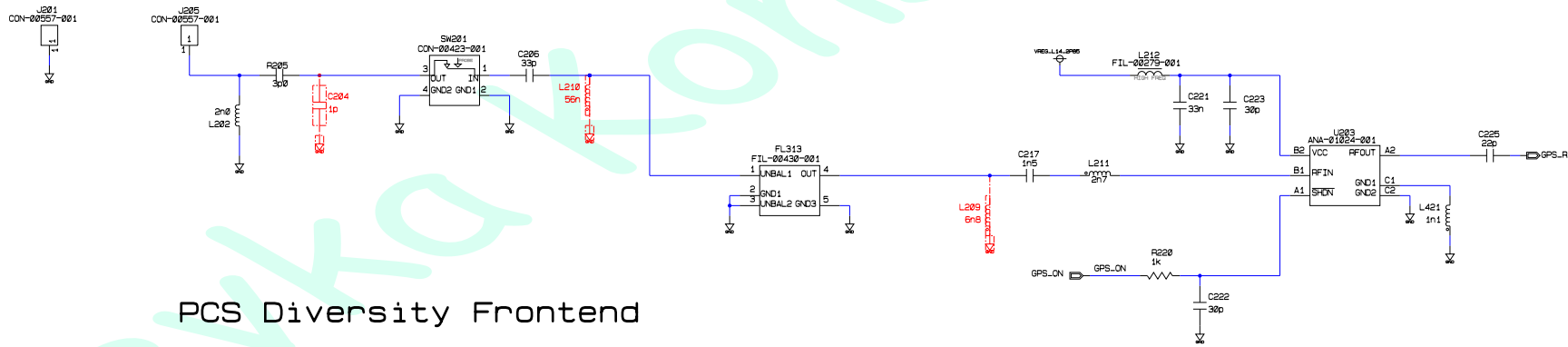
ANT\_GND\_TUNE\_2 out port has been removed due to removal of charging contact.



### GPS Frontend

GPS GND

GPS ANTENNA

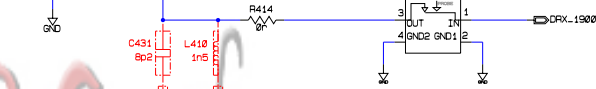


### PCS Diversity Frontend

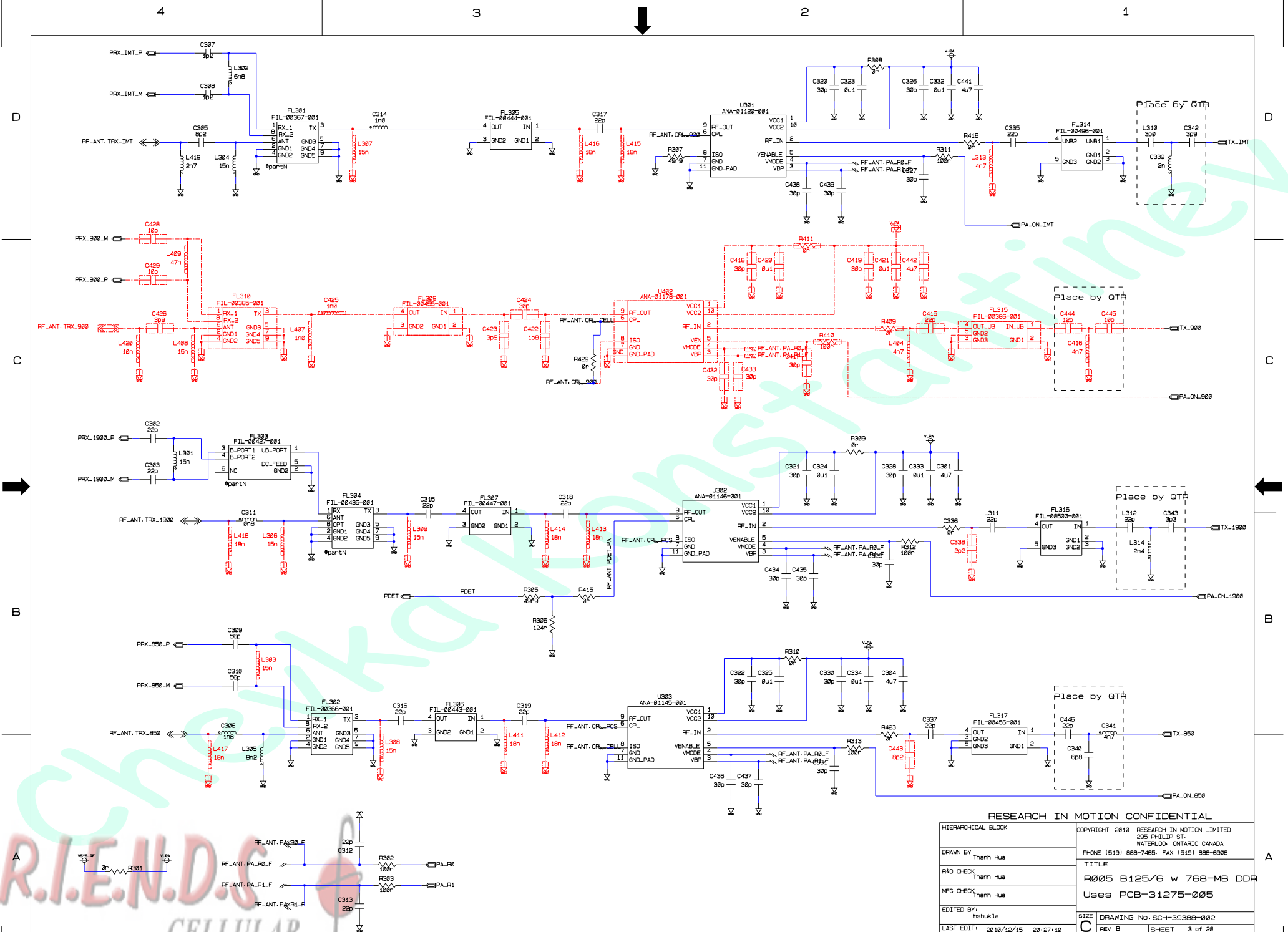
CON-00421-001

CON-00421-001

CON-00423-001



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MFG CHECK Thanh Hua	Uses PCB-31275-005
EDITED BY: y111	SIZE C
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MFG CHECK Thanh Hua	Uses PCB-31275-005
EDITED BY: nshukla	SIZE C DRAWING NO. SCH-39388-002
LAST EDIT: 2010/12/15 20:27:10	REV B SHEET 3 of 20

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D

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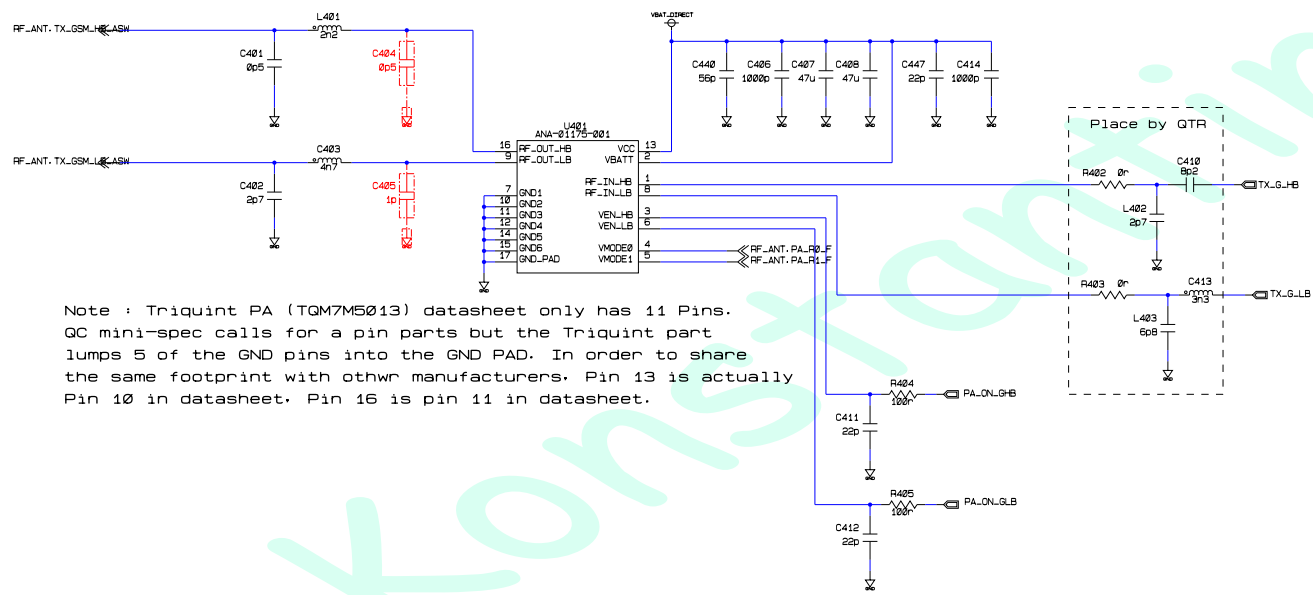
C

B

B

A

A



Note : Triquint PA (TQM7M5013) datasheet only has 11 Pins. QC mini-spec calls for a pin parts but the Triquint part lumps 5 of the GND pins into the GND PAD. In order to share the same footprint with other manufacturers. Pin 13 is actually Pin 10 in datasheet. Pin 16 is pin 11 in datasheet.

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Zuken CR-5000 System Designer

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MFG CHECK Thanh Hua		
EDITED BY: tyoung	SIZE C	DRAWING No. SCH-39388-002
LAST EDIT: 2010/12/15 20:27:10	REV B	SHEET 4 of 20

D

D

C

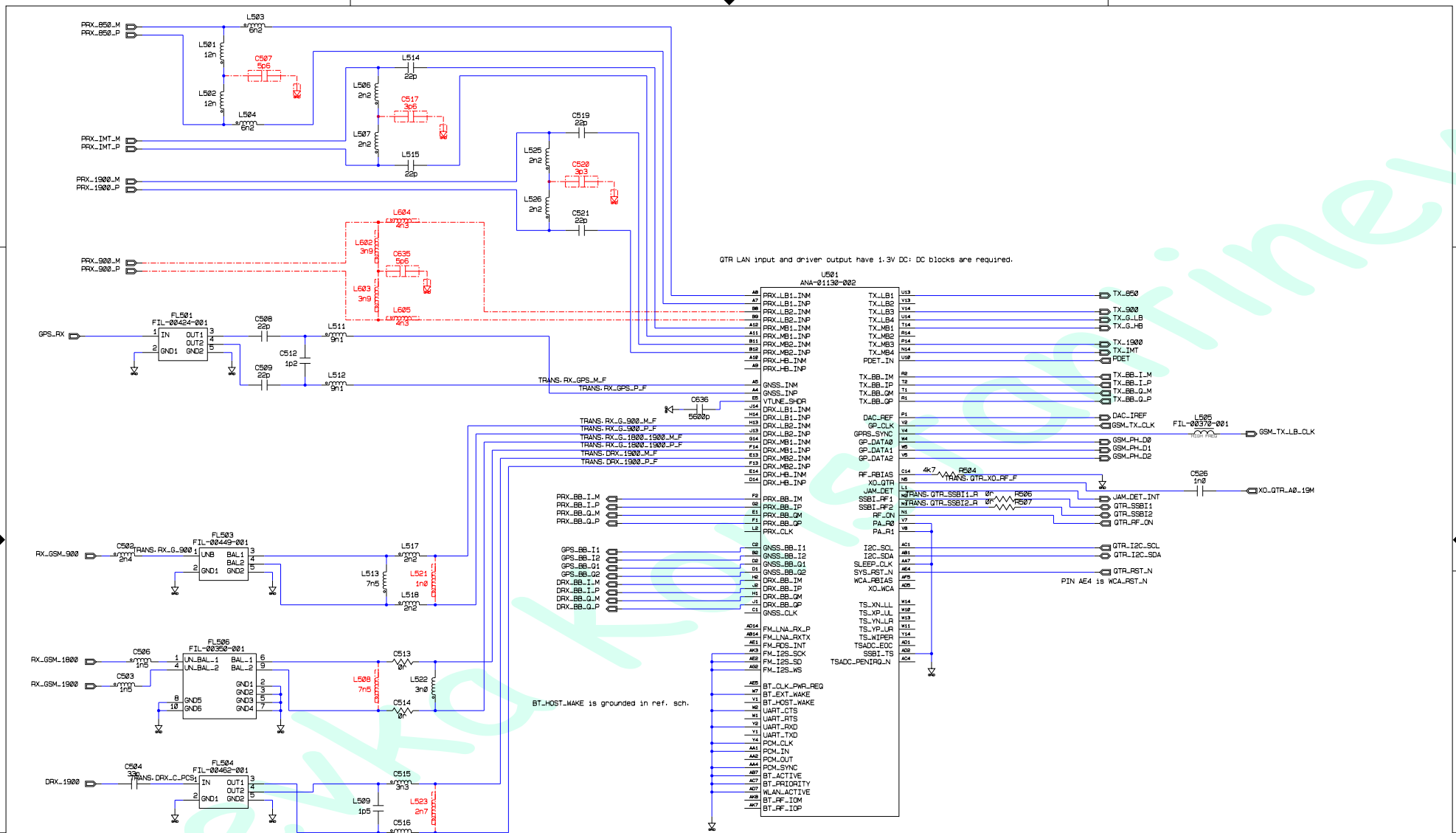
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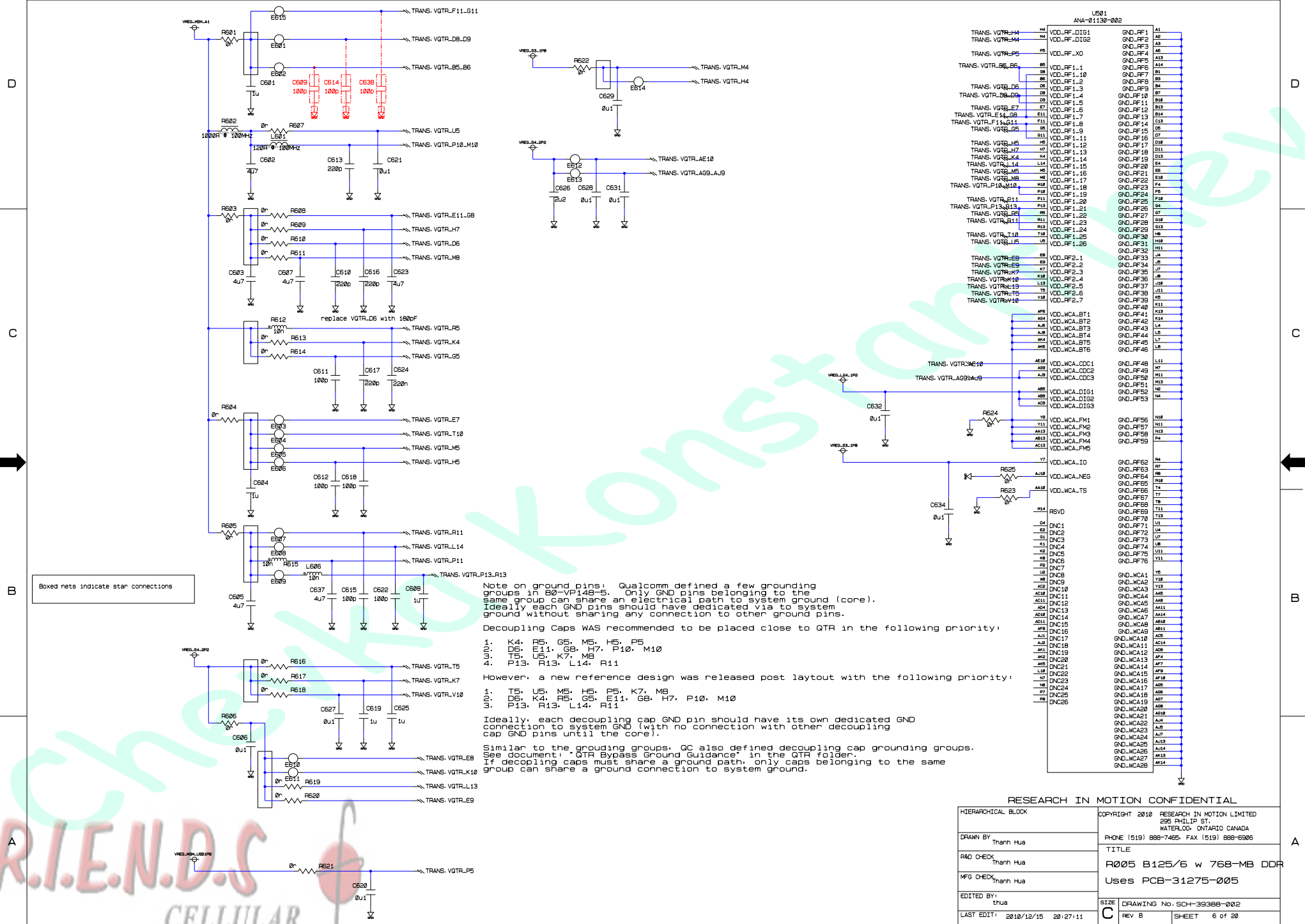
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MFG CHECK Thanh Hua	
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Boxed nets indicate star connections

Note on ground pins: Qualcomm defined a few grounding groups in 80-VI48-5. Only GND pins belonging to the same group can share an electrical path to system ground (core). Ideally each GND pins should have dedicated via to system ground without sharing any connection to other ground pins.

Decoupling Caps WAS recommended to be placed close to QTR in the following priority:

1. K4, R5, G5, M5, H5, P5
2. D6, E11, G8, H7, P10, M10
3. T5, U5, K7, M8
4. P13, R13, L14, R11

However, a new reference design was released post layout with the following priority:

1. T5, U5, M5, H5, P5, K7, M8
2. D6, K4, R5, G5, E11, G8, H7, P10, M10
3. P13, R13, L14, R11

Ideally, each decoupling cap GND pin should have its own dedicated GND connection to system GND (with no connection with other decoupling cap GND pins until the core).

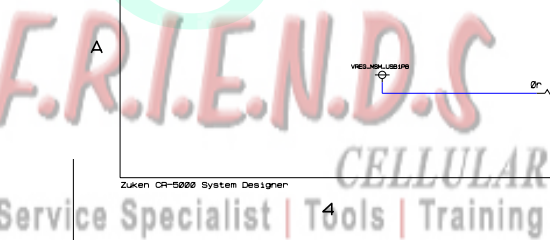
Similar to the grouping groups, QC also defined decoupling cap grounding groups. See document: QTR Bypass Ground Guidance in the QTR folder. If decoupling caps must share a ground path, only caps belonging to the same group can share a ground connection to system ground.

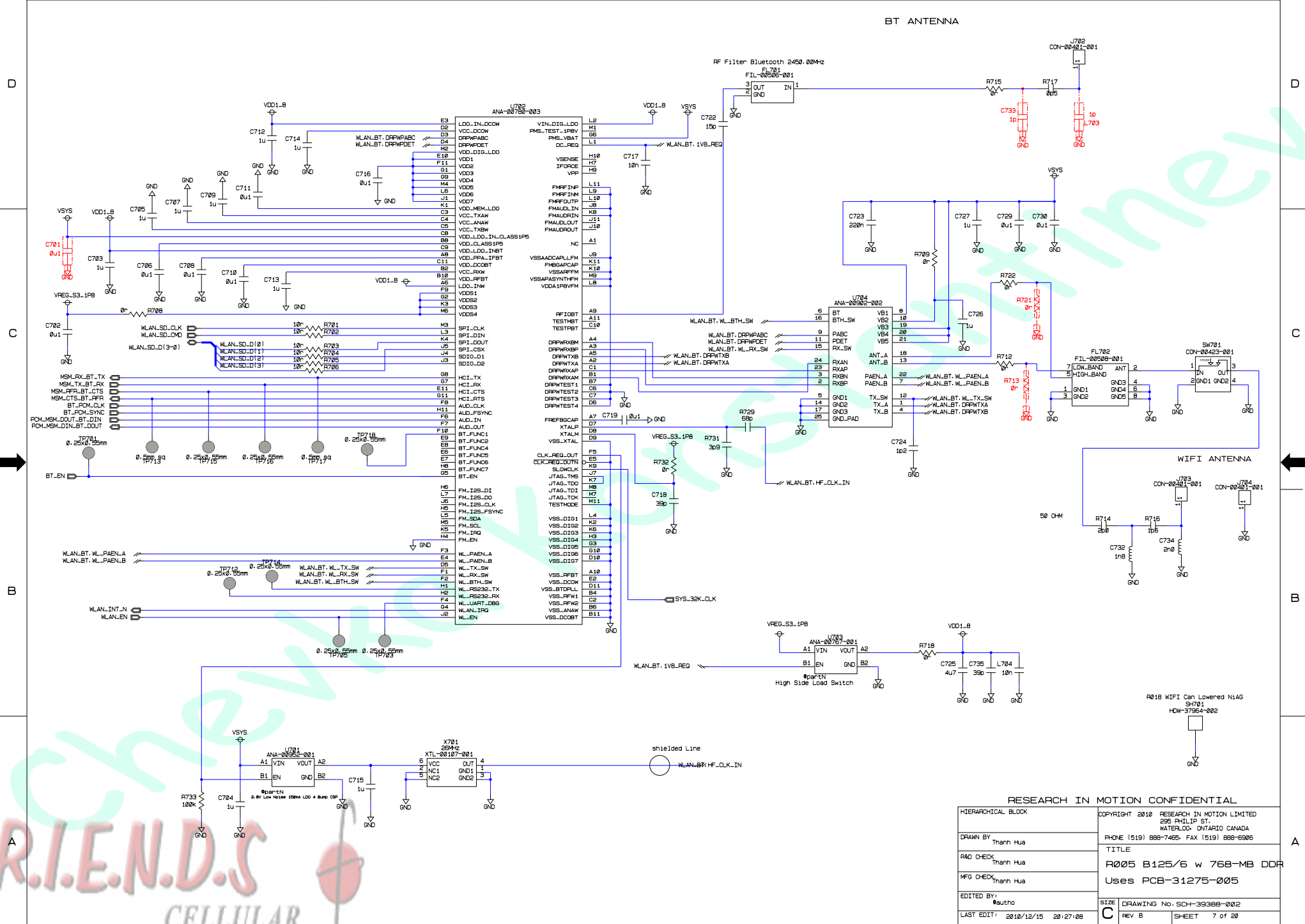
U501 ANA-01130-002

TRANS_VQTR_H4	H4	VDD_RF1.DIG1	A1
TRANS_VQTR_M4	M4	VDD_RF1.DIG2	A2
TRANS_VQTR_P5	P5	GND_RF3	A3
TRANS_VQTR_S6_B6	S6	GND_RF4	A4
TRANS_VQTR_S6_B6	S6	GND_RF5	A5
TRANS_VQTR_S6_B6	S6	GND_RF6	A6
TRANS_VQTR_S6_B6	S6	GND_RF7	A7
TRANS_VQTR_S6_B6	S6	GND_RF8	A8
TRANS_VQTR_S6_B6	S6	GND_RF9	A9
TRANS_VQTR_S6_B6	S6	GND_RF10	A10
TRANS_VQTR_S6_B6	S6	GND_RF11	A11
TRANS_VQTR_S6_B6	S6	GND_RF12	A12
TRANS_VQTR_S6_B6	S6	GND_RF13	A13
TRANS_VQTR_S6_B6	S6	GND_RF14	A14
TRANS_VQTR_S6_B6	S6	GND_RF15	A15
TRANS_VQTR_S6_B6	S6	GND_RF16	A16
TRANS_VQTR_S6_B6	S6	GND_RF17	A17
TRANS_VQTR_S6_B6	S6	GND_RF18	A18
TRANS_VQTR_S6_B6	S6	GND_RF19	A19
TRANS_VQTR_S6_B6	S6	GND_RF20	A20
TRANS_VQTR_S6_B6	S6	GND_RF21	A21
TRANS_VQTR_S6_B6	S6	GND_RF22	A22
TRANS_VQTR_S6_B6	S6	GND_RF23	A23
TRANS_VQTR_S6_B6	S6	GND_RF24	A24
TRANS_VQTR_S6_B6	S6	GND_RF25	A25
TRANS_VQTR_S6_B6	S6	GND_RF26	A26
TRANS_VQTR_S6_B6	S6	GND_RF27	A27
TRANS_VQTR_S6_B6	S6	GND_RF28	A28
TRANS_VQTR_S6_B6	S6	GND_RF29	A29
TRANS_VQTR_S6_B6	S6	GND_RF30	A30
TRANS_VQTR_S6_B6	S6	GND_RF31	A31
TRANS_VQTR_S6_B6	S6	GND_RF32	A32
TRANS_VQTR_S6_B6	S6	GND_RF33	A33
TRANS_VQTR_S6_B6	S6	GND_RF34	A34
TRANS_VQTR_S6_B6	S6	GND_RF35	A35
TRANS_VQTR_S6_B6	S6	GND_RF36	A36
TRANS_VQTR_S6_B6	S6	GND_RF37	A37
TRANS_VQTR_S6_B6	S6	GND_RF38	A38
TRANS_VQTR_S6_B6	S6	GND_RF39	A39
TRANS_VQTR_S6_B6	S6	GND_RF40	A40
TRANS_VQTR_S6_B6	S6	GND_RF41	A41
TRANS_VQTR_S6_B6	S6	GND_RF42	A42
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TRANS_VQTR_S6_B6	S6	GND_RF55	A55
TRANS_VQTR_S6_B6	S6	GND_RF56	A56
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TRANS_VQTR_S6_B6	S6	GND_RF82	A82
TRANS_VQTR_S6_B6	S6	GND_RF83	A83
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TRANS_VQTR_S6_B6	S6	GND_RF90	A90
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TRANS_VQTR_S6_B6	S6	GND_RF93	A93
TRANS_VQTR_S6_B6	S6	GND_RF94	A94
TRANS_VQTR_S6_B6	S6	GND_RF95	A95
TRANS_VQTR_S6_B6	S6	GND_RF96	A96
TRANS_VQTR_S6_B6	S6	GND_RF97	A97
TRANS_VQTR_S6_B6	S6	GND_RF98	A98
TRANS_VQTR_S6_B6	S6	GND_RF99	A99
TRANS_VQTR_S6_B6	S6	GND_RF100	A100

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LAST EDIT:	SIZE DRAWING No. SCH-39388-002
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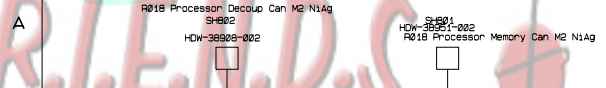
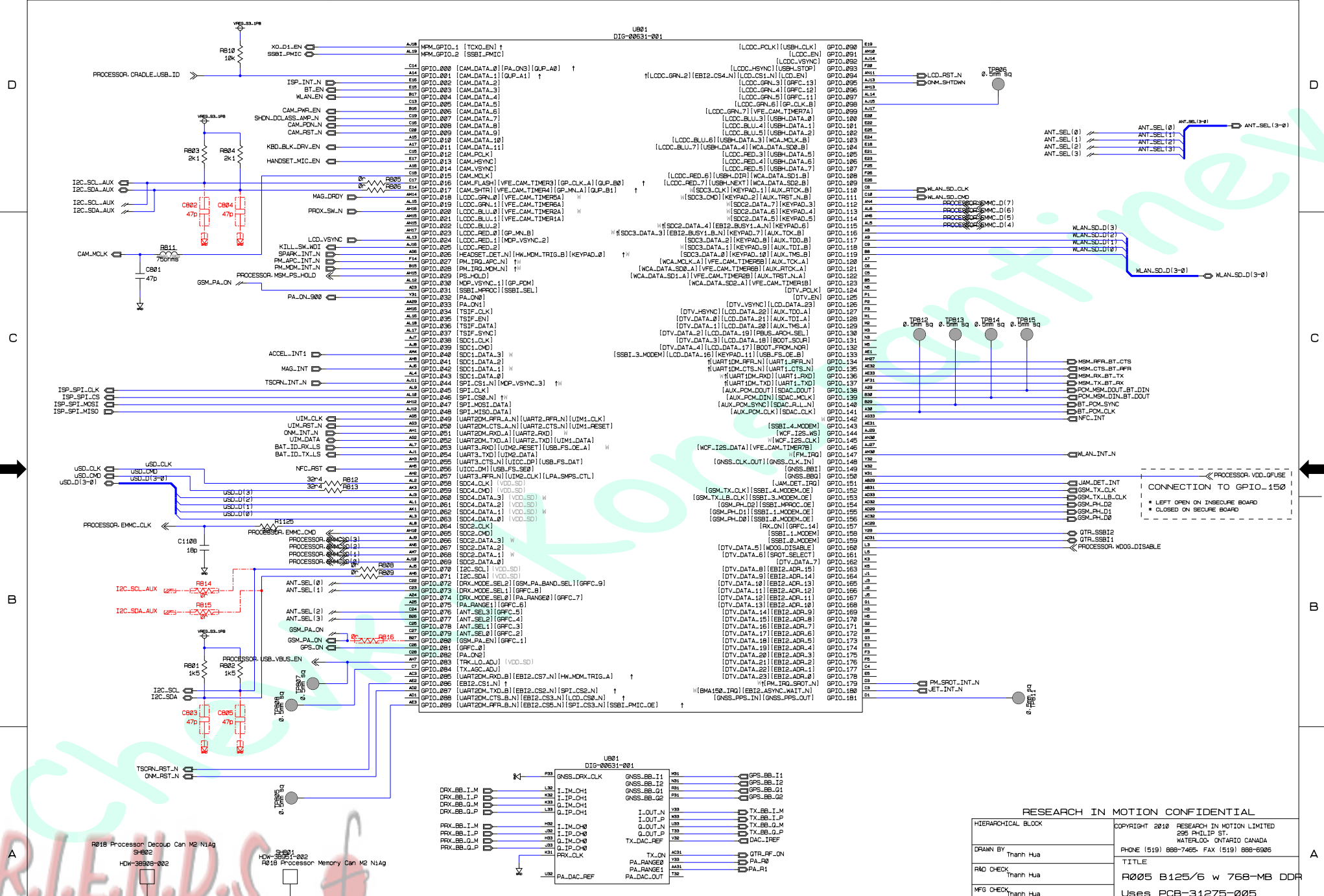




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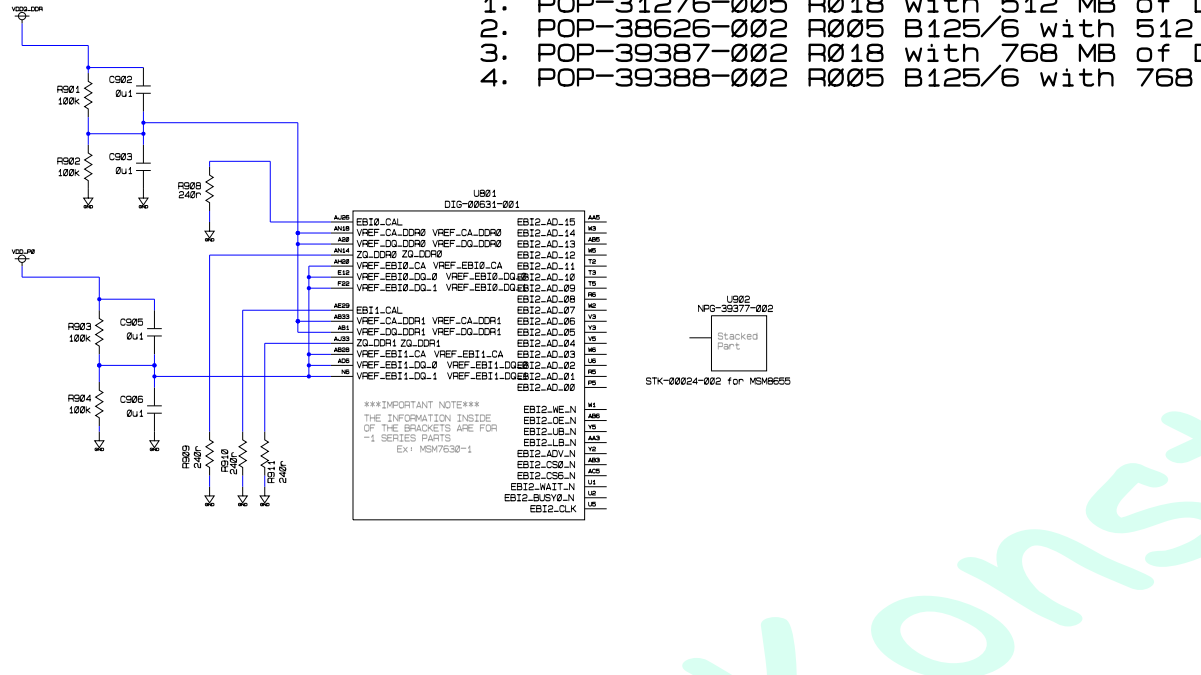
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Uses PCB	31275-005
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REV	B
SHEET	8 of 20



# PROCESSOR/MEMORY INTERFACE

Design notes for Design that uses PCB revision-5:

- 1. POP-31276-005 R018 with 512 MB of DDR Memory
- 2. POP-38626-002 R005 B125/6 with 512 MB of DDR Memory
- 3. POP-39387-002 R018 with 768 MB of DDR Memory with 0.9-mm heigh
- 4. POP-39388-002 R005 B125/6 with 768 MB of DDR Memory with 0.9-mm heigh

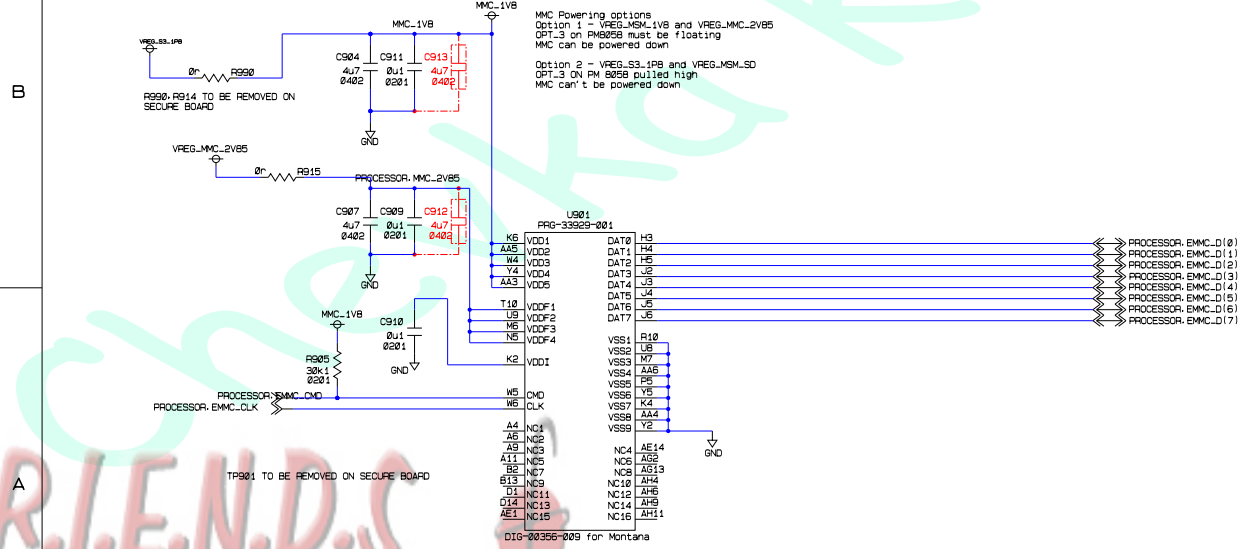


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DIG-02631-001

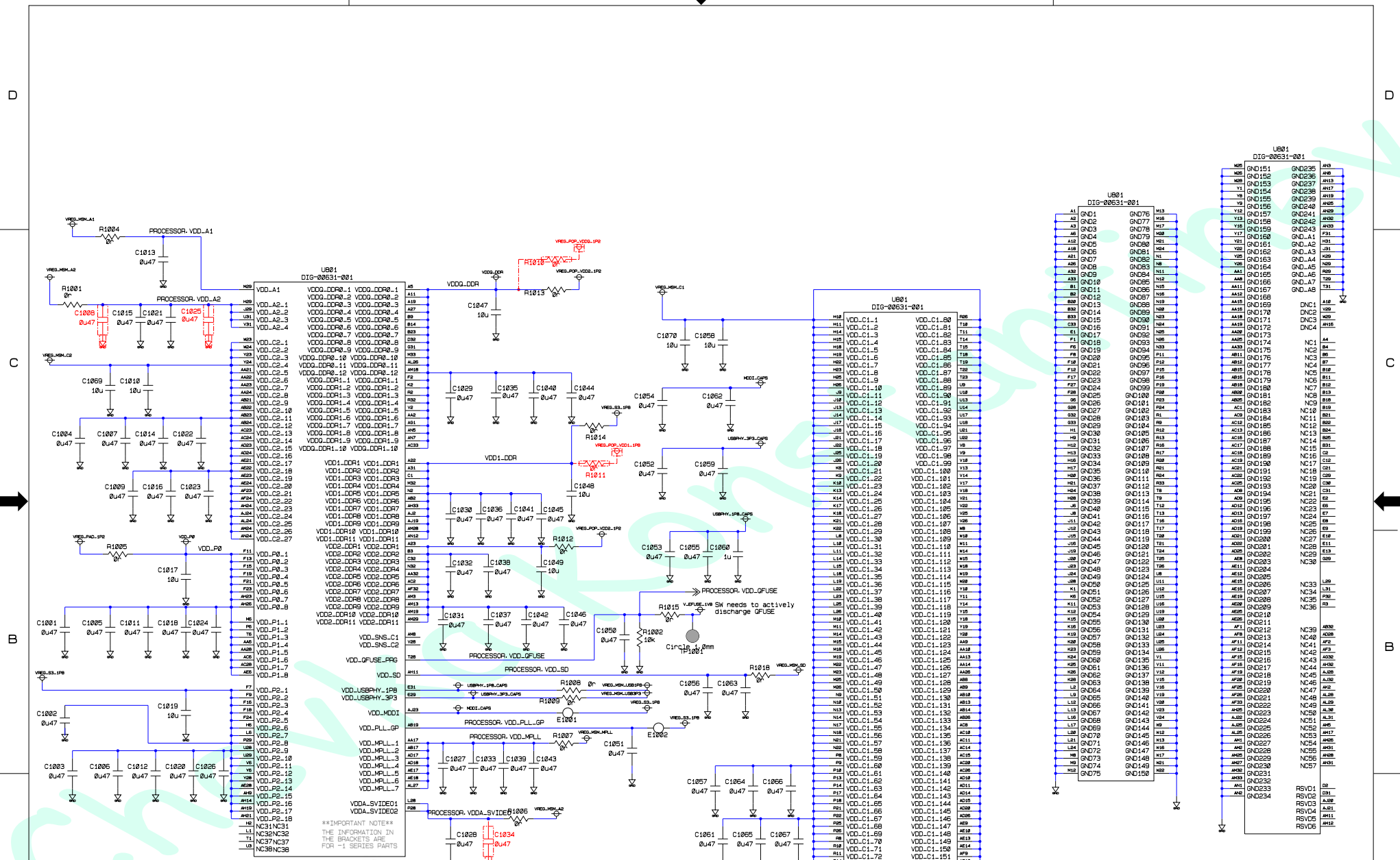
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AH10	VREF_CA_DDR0	VREF_CA_DDR0	EB12_AD_14	AE2	
AS10	VREF_DQ_DDR0	VREF_DQ_DDR0	EB12_AD_13	AE3	
AK10	ZQ_DDR0	ZQ_DDR0	EB12_AD_12	AE4	
EL10	VREF_EB10_CA	VREF_EB10_CA	EB12_AD_11	AE5	
FB10	VREF_EB10_DQ_0	VREF_EB10_DQ_0	EB12_AD_10	AE6	
AE9	VREF_EB10_DQ_1	VREF_EB10_DQ_1	EB12_AD_09	AE7	
AE8	EB11_CAL	EB12_AD_08	AE8		
AE7	VREF_CA_DDR1	VREF_CA_DDR1	EB12_AD_07	AE9	
AE6	VREF_DQ_DDR1	VREF_DQ_DDR1	EB12_AD_06	AE10	
AE5	ZQ_DDR1	ZQ_DDR1	EB12_AD_05	AE11	
AE4	VREF_EB11_CA	VREF_EB11_CA	EB12_AD_04	AE12	
AE3	VREF_EB11_DQ_0	VREF_EB11_DQ_0	EB12_AD_03	AE13	
AE2	VREF_EB11_DQ_1	VREF_EB11_DQ_1	EB12_AD_02	AE14	
AE1	VREF_EB11_DQ_2	VREF_EB11_DQ_2	EB12_AD_01	AE15	
AE0	EB12_AD_00	EB12_AD_00	AE16		
AE17	EB12_WE_N	AE17			
AE18	EB12_CE_N	AE18			
AE19	EB12_CS0_N	AE19			
AE20	EB12_CS1_N	AE20			
AE21	EB12_CS2_N	AE21			
AE22	EB12_CS3_N	AE22			
AE23	EB12_WALT_N	AE23			
AE24	EB12_BSV0_N	AE24			
AE25	EB12_CLK	AE25			

U902  
NPG-39377-002  
Stacked Part  
STK-00024-002 for MSM8655

\*\*\*IMPORTANT NOTE\*\*\*  
THE INFORMATION INSIDE OF THE BRACKETS ARE FOR -1 SERIES PARTS  
EX: MSM7630-1



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MFG CHECK	Thanh Hua	R005 B125/6 w 768-MB DDR
		Uses PCB-31275-005
EDITED BY:	y111	SIZE
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		REV B
		SHEET 10 of 20



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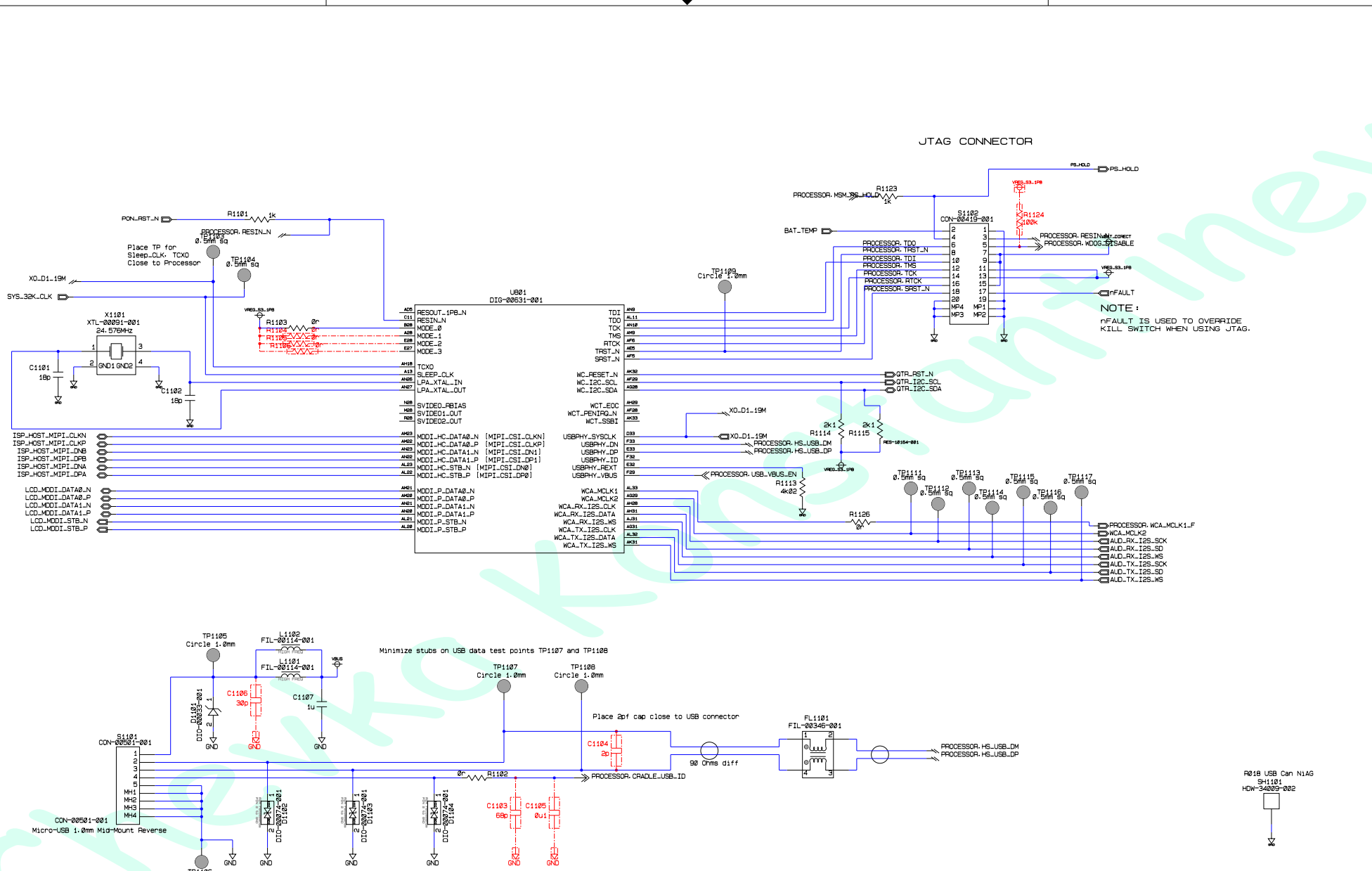
C

B

B

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A



**JTAG CONNECTOR**

NOTE:  
nFAULT IS USED TO OVERRIDE  
KILL SWITCH WHEN USING JTAG.

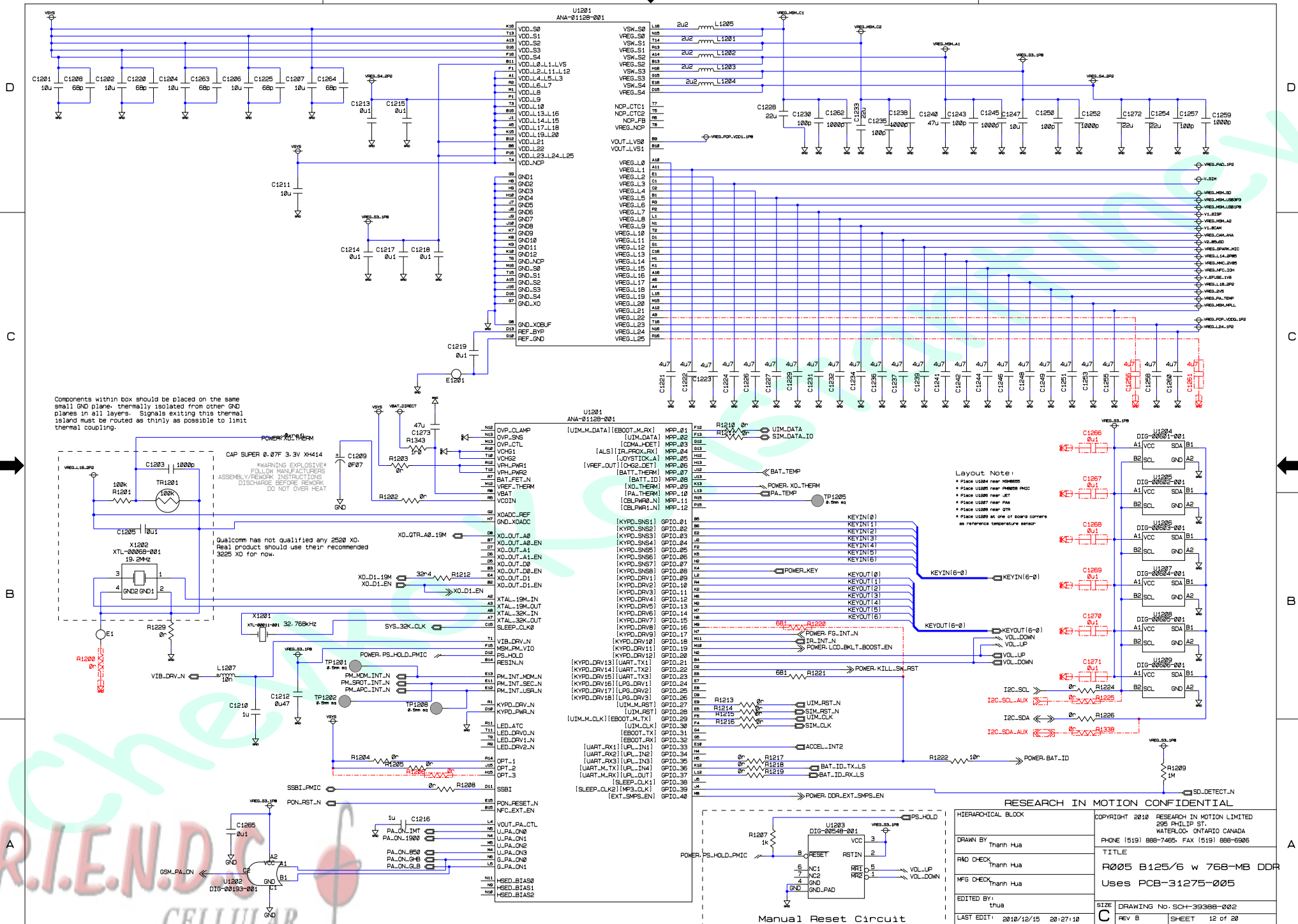
Minimize stubs on USB data test points TP1107 and TP1108

Place 20p cap close to USB connector

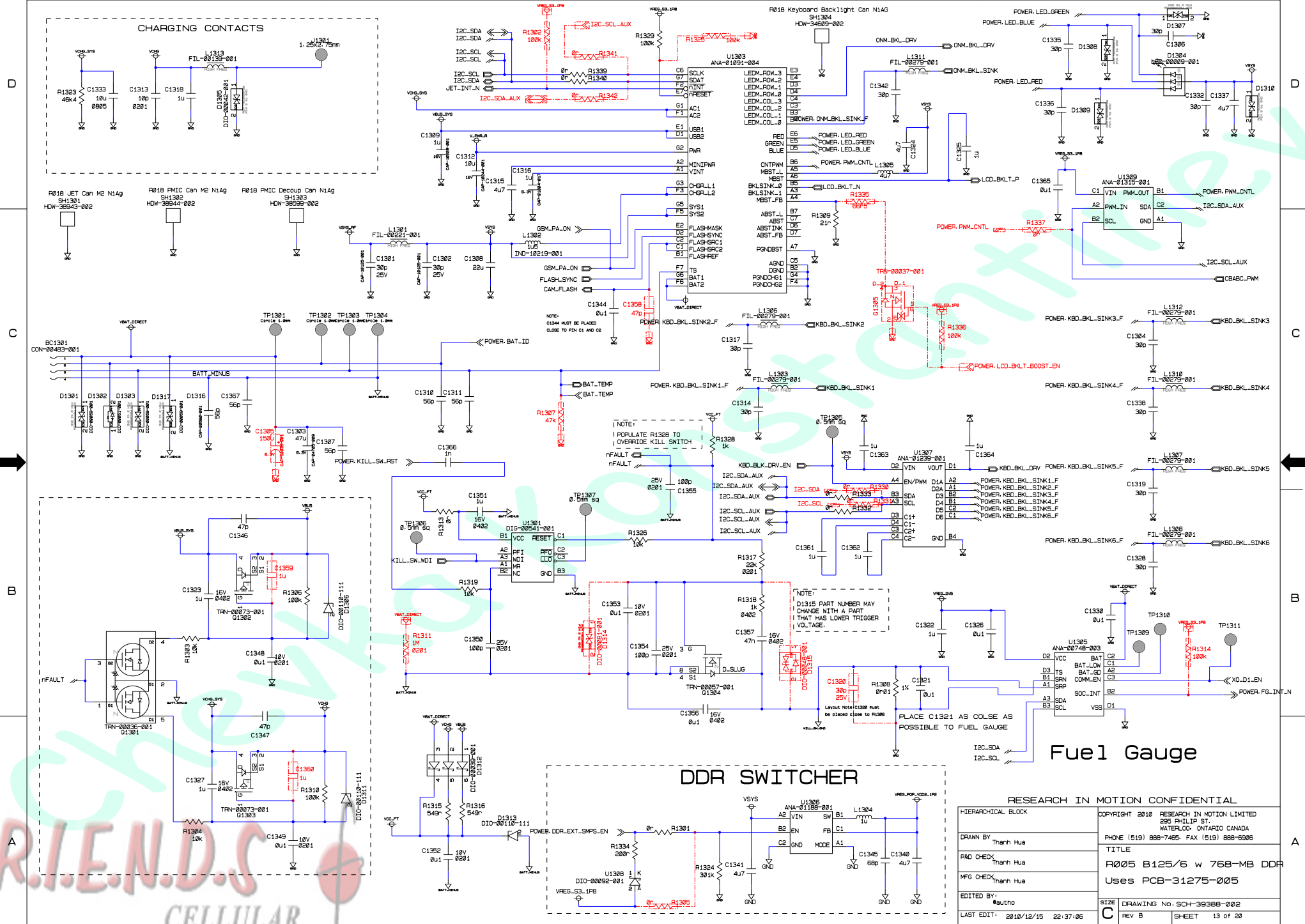
R019 USB Cap NIAG  
S1101  
HW-34023-002

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MFG CHECK Thanh Hua	
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SIZE C	DRAWING No. SCH-39388-002
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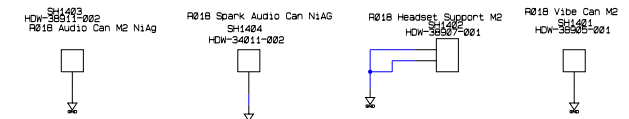
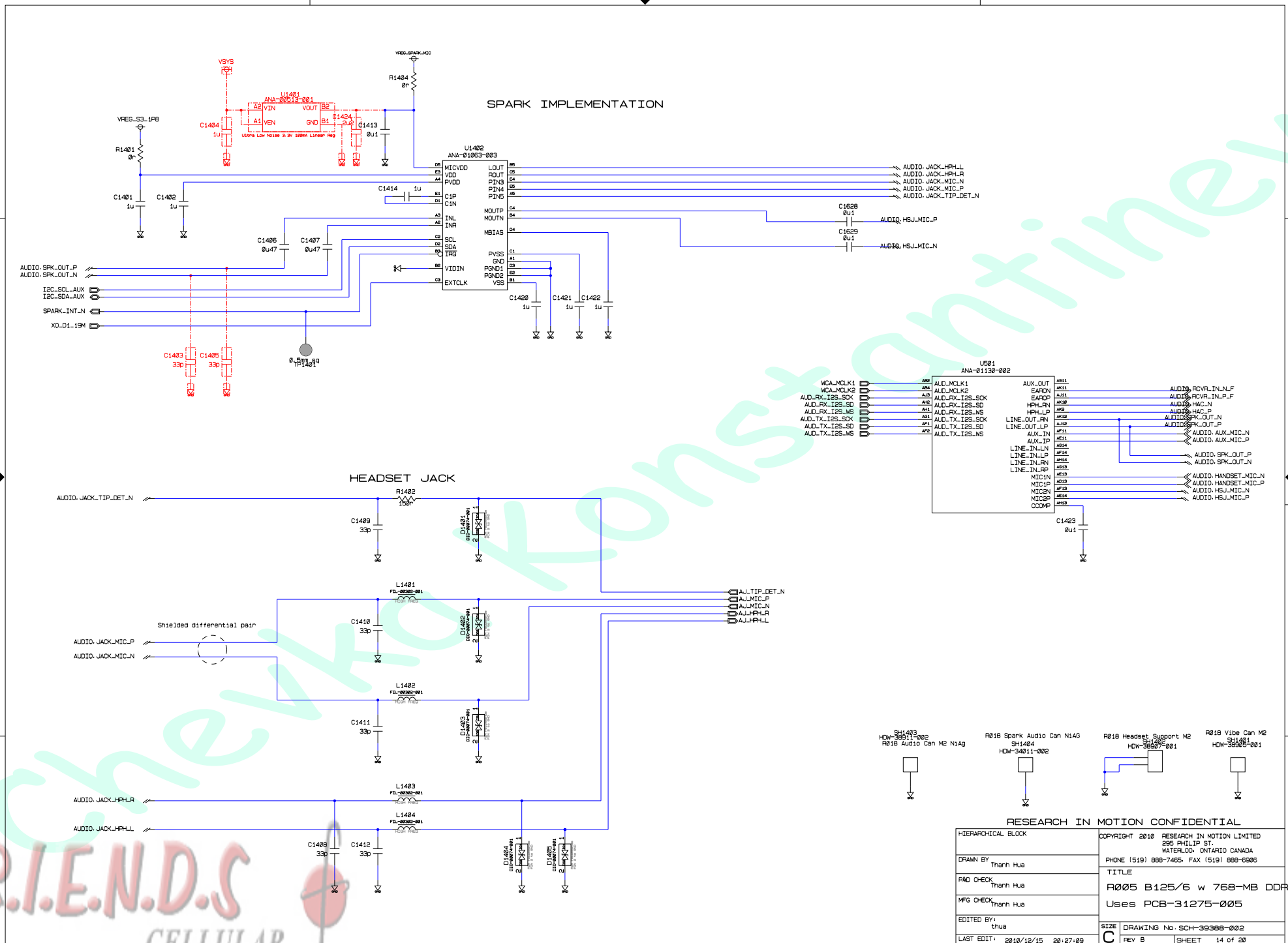


### Fuel Gauge

### DDR SWITCHER

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MFG CHECK Thanh Hua	Uses PCB-31275-005
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LAST EDIT: 2010/12/15 22:37:06	DRAWING No. SCH-39388-002
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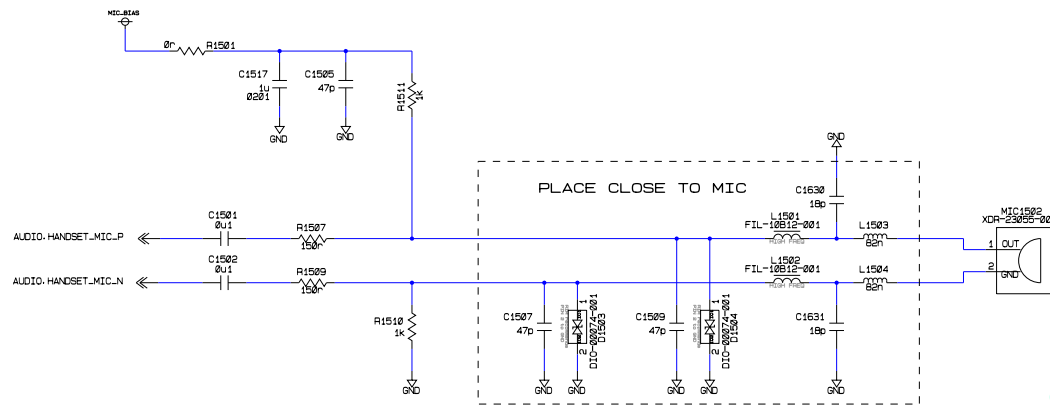


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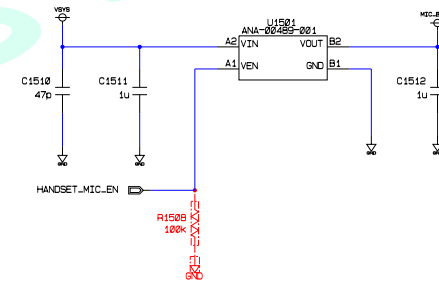
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MFG CHECK Thanh Hua	Uses PCB-31275-005
EDITED BY: thua	SIZE DRAWING No. SCH-39388-002
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thua 2009/07/08

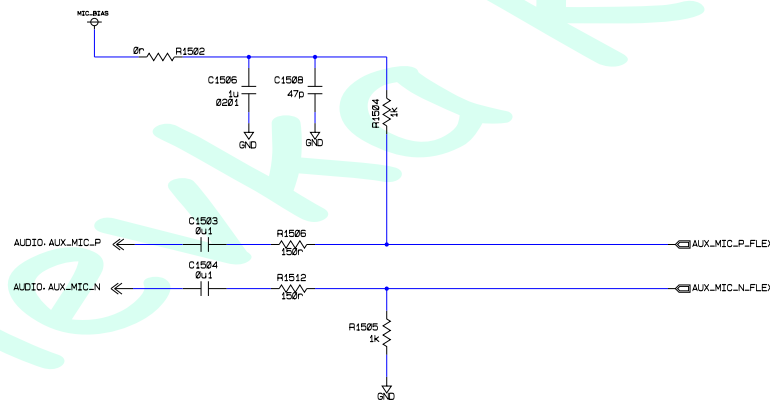
### Main MIC



### MIC BIAS



### AUX MIC



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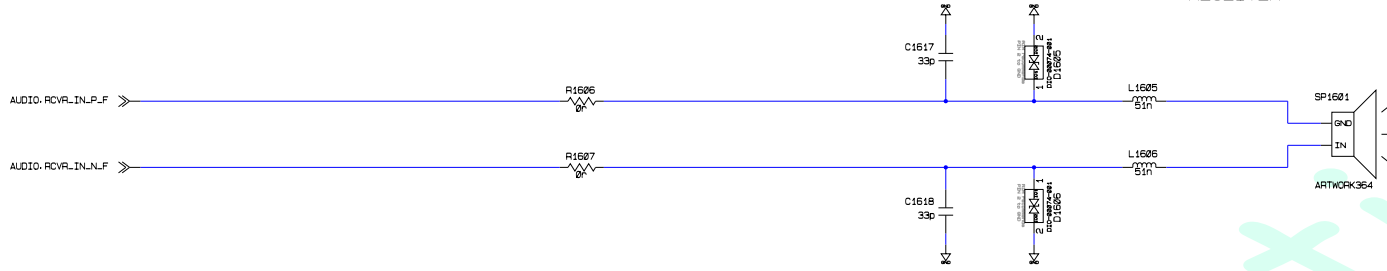
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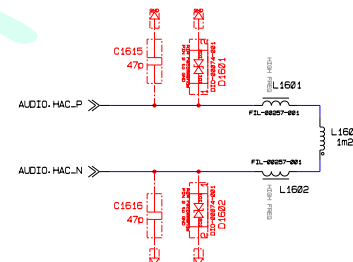
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AUDIO RECEIVER

RECEIVER

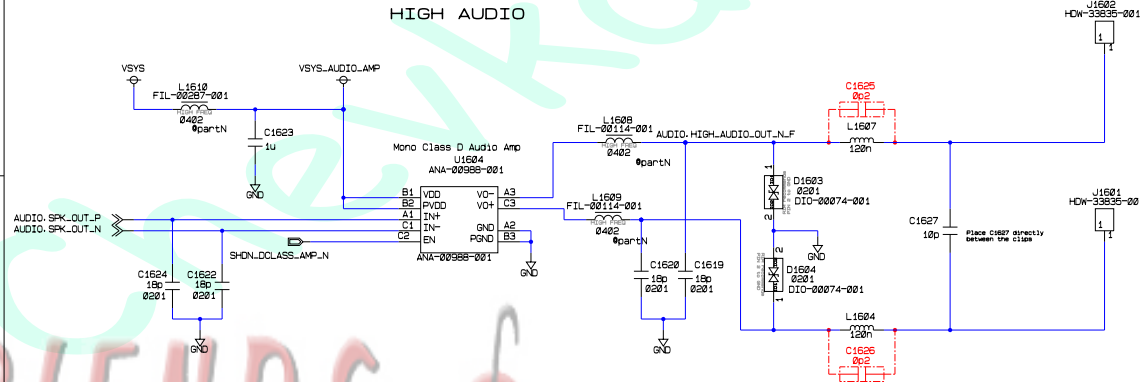


AUDIO HAC COIL



PLEASE CHECK HAC POLARITY!

HIGH AUDIO

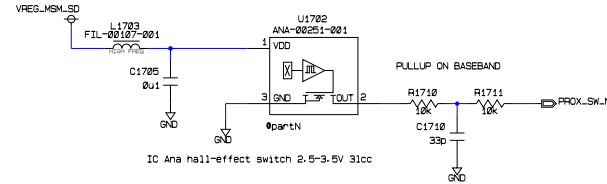


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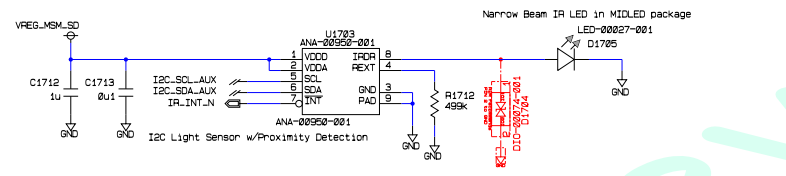
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R&D CHECK Thanh Hua	Uses PCB-31275-005
MFG CHECK Thanh Hua	
EDITED BY: tyoung	SIZE DRAWING No. SCH-39388-002
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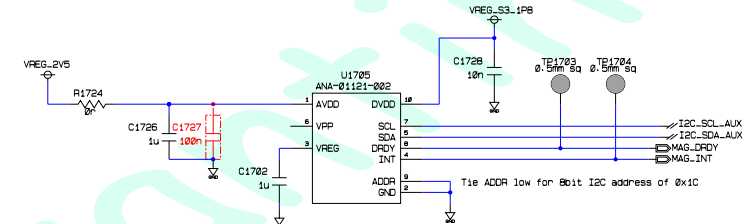
### HOLSTER SENSOR



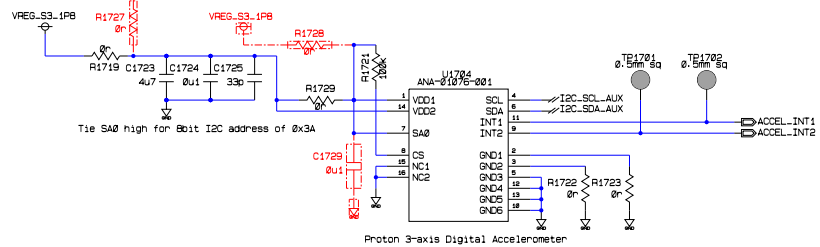
### IR+LIGHT SENSOR



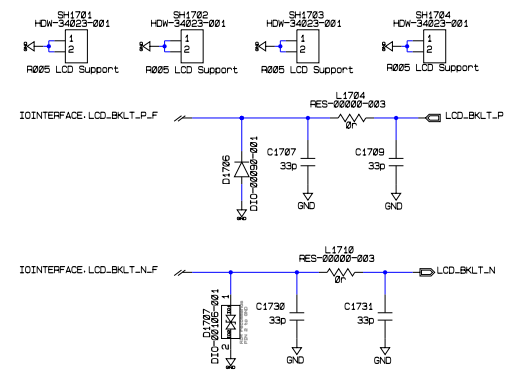
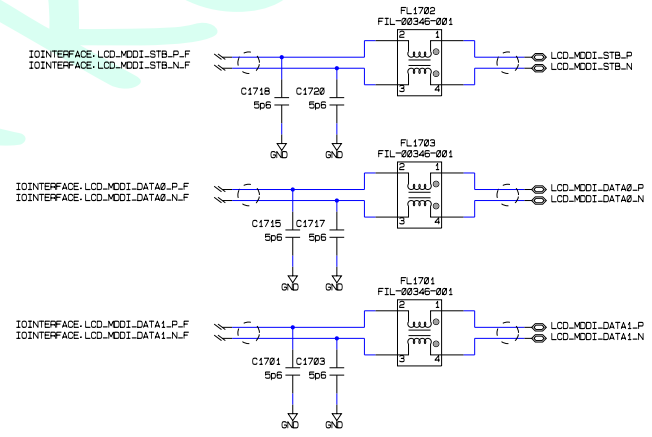
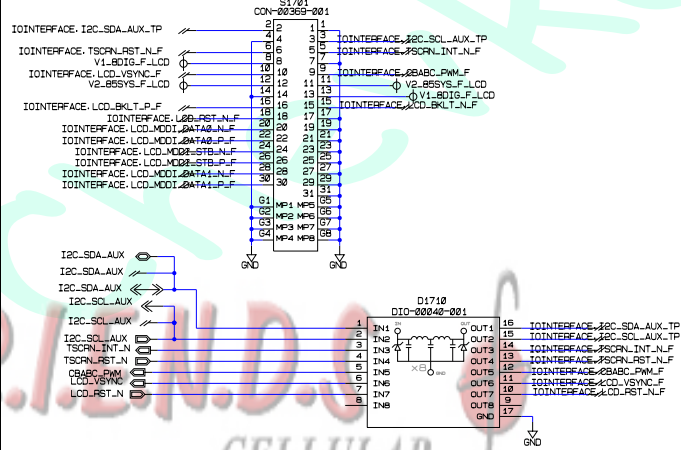
### AMI306 Tri-Axis Magnetometer



### Tri-Axis Accelerometer

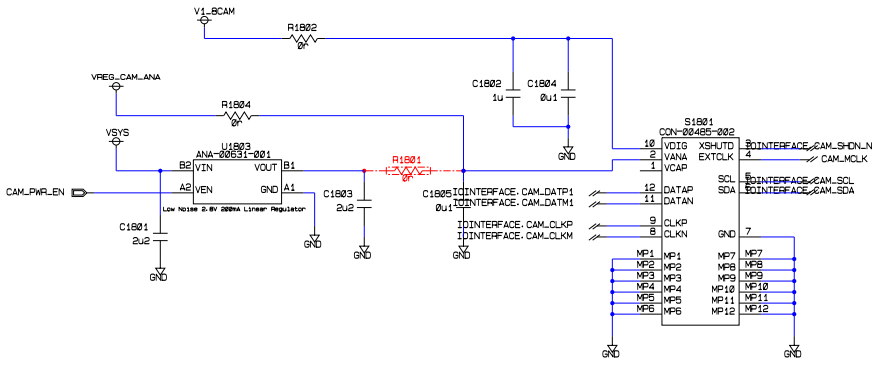


### LCD/TOUCH PANEL CONNECTOR

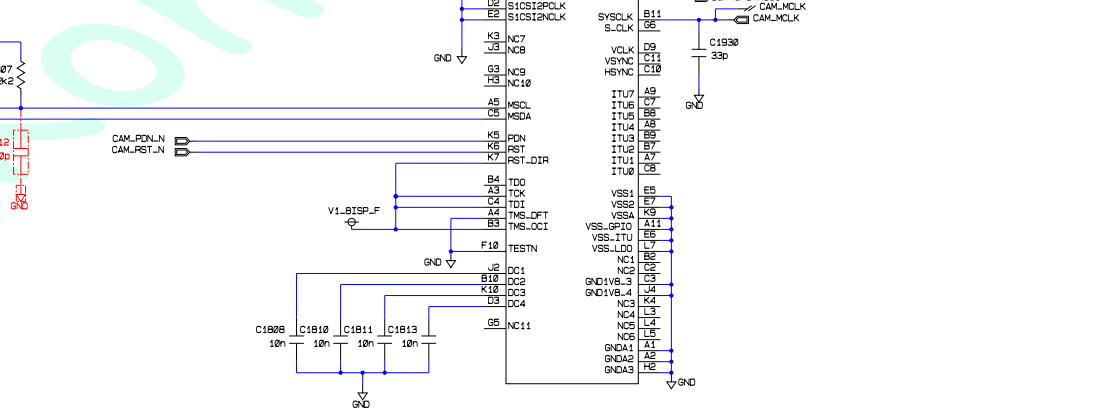
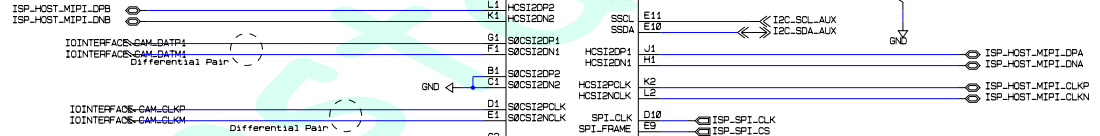
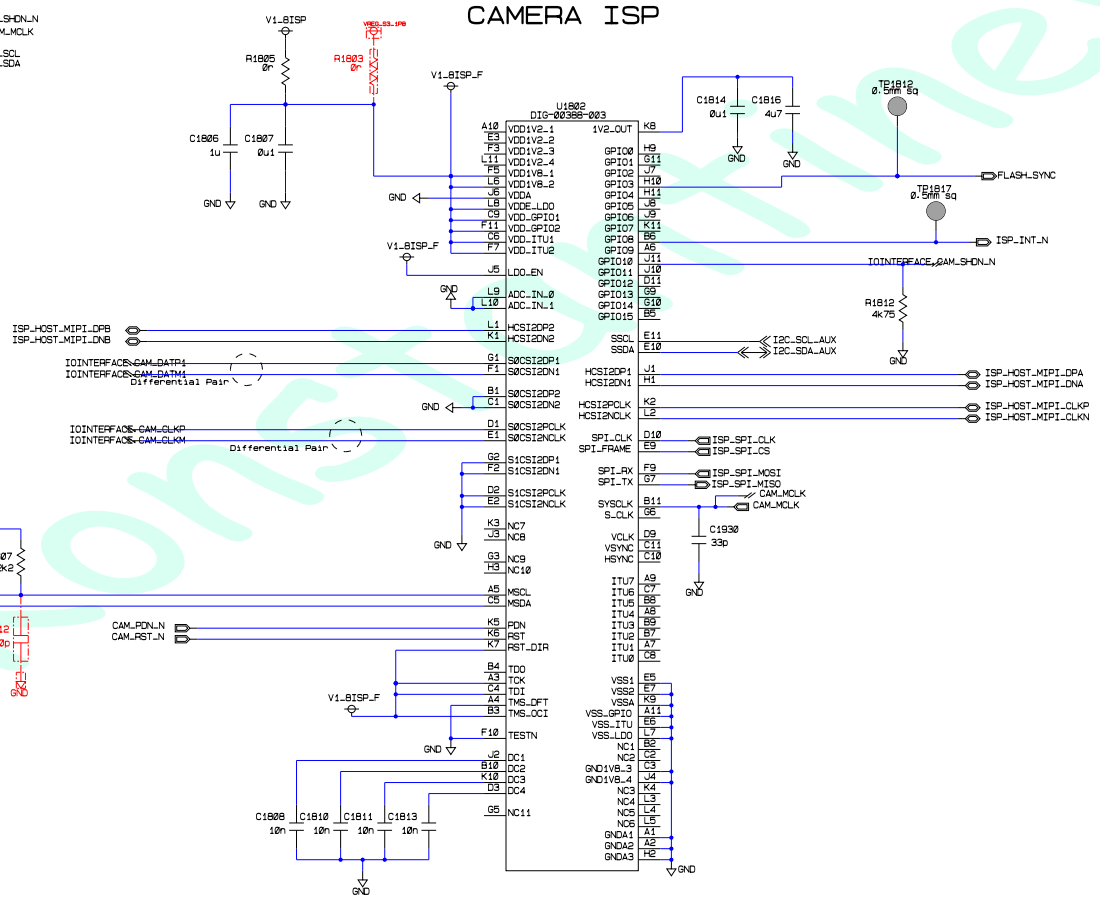


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### 5MP FALCON CAMERA SOCKET



### CAMERA ISP

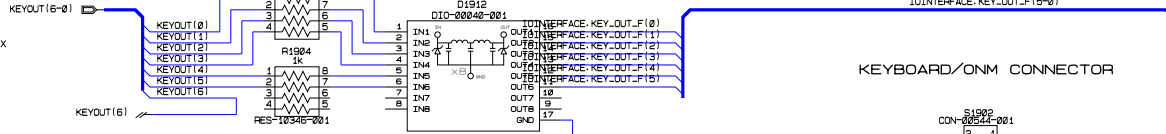
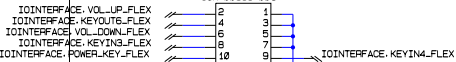


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MFG CHECK Thanh Hua	Uses PCB-31275-005
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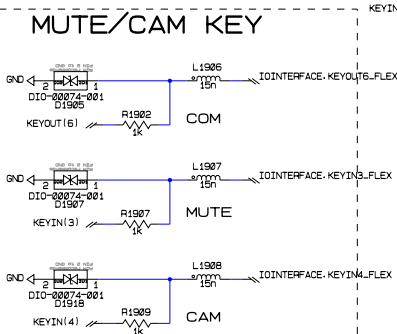
SIDE KEY FLEX CONNECTOR



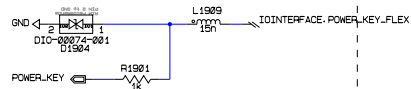
KEYBOARD/ONM CONNECTOR



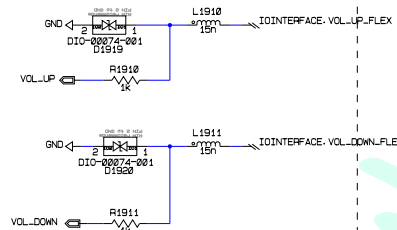
MUTE/CAM KEY



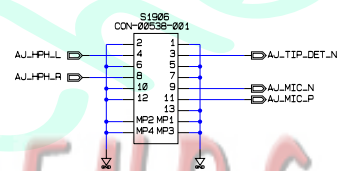
POWER KEY



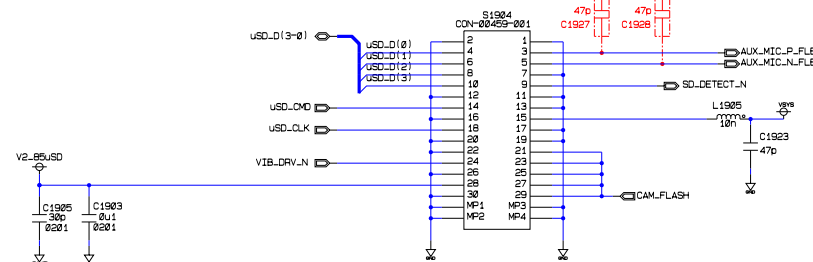
VOLUME KEY



AUDIO HEADSET JACK CONNECTION

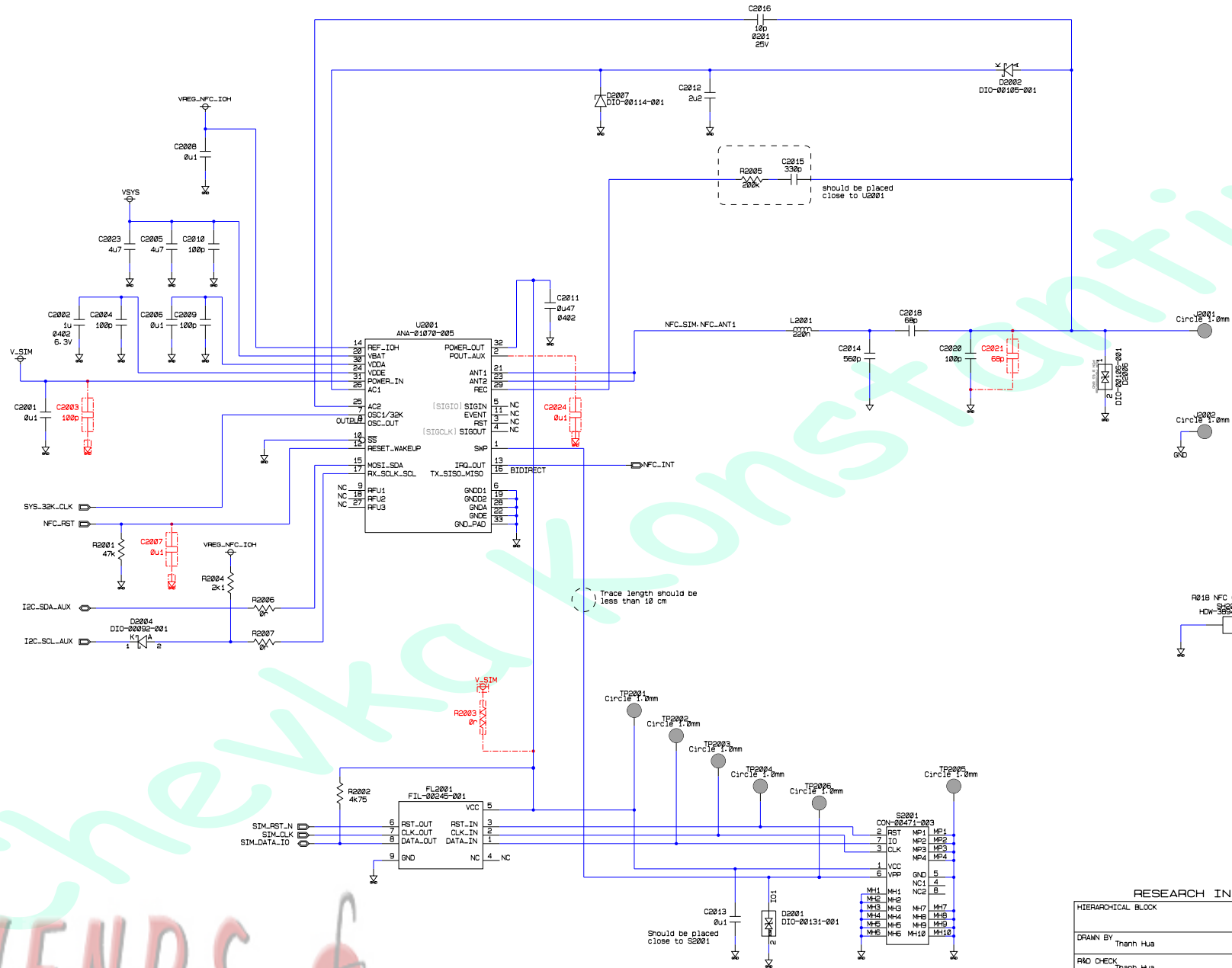


DAUGHTER BOARD CONNECTORS



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RWD CHECK: Thanh Hua	Uses PCB-31275-005
MFG CHECK: Thanh Hua	
EDITED BY: thua	SIZE: DRAWING No. SCH-39388-002
LAST EDIT: 2010/12/15 20:27:08	REV B SHEET 20 of 20